

Title: Virtual Machine Interface For Hardware Reconfigurable And Software Programmable Processors

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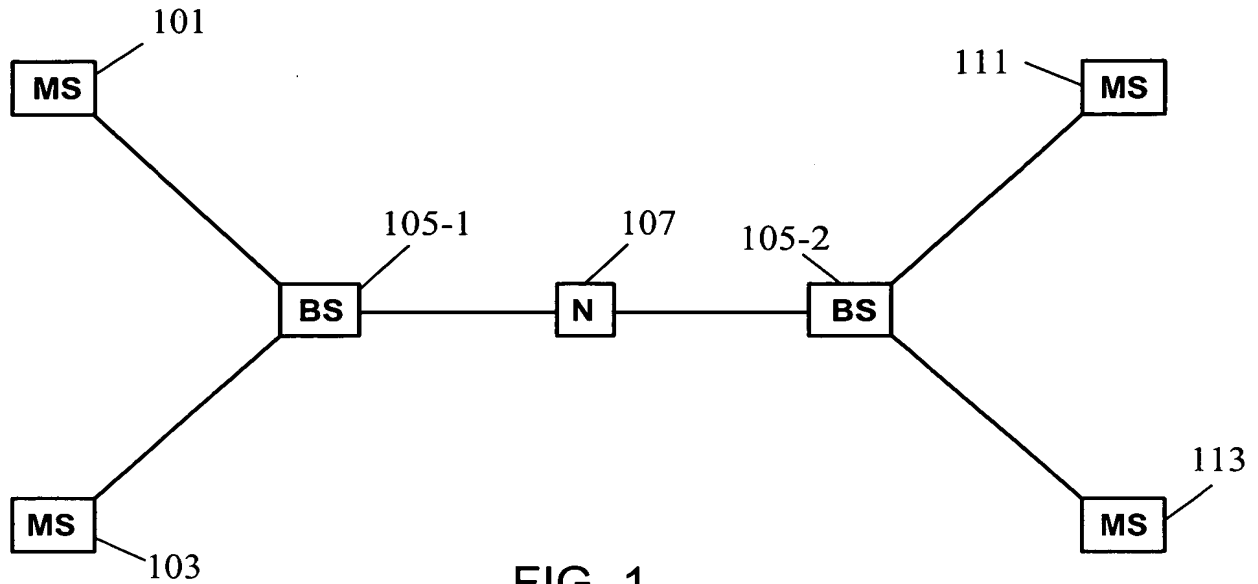


FIG. 1

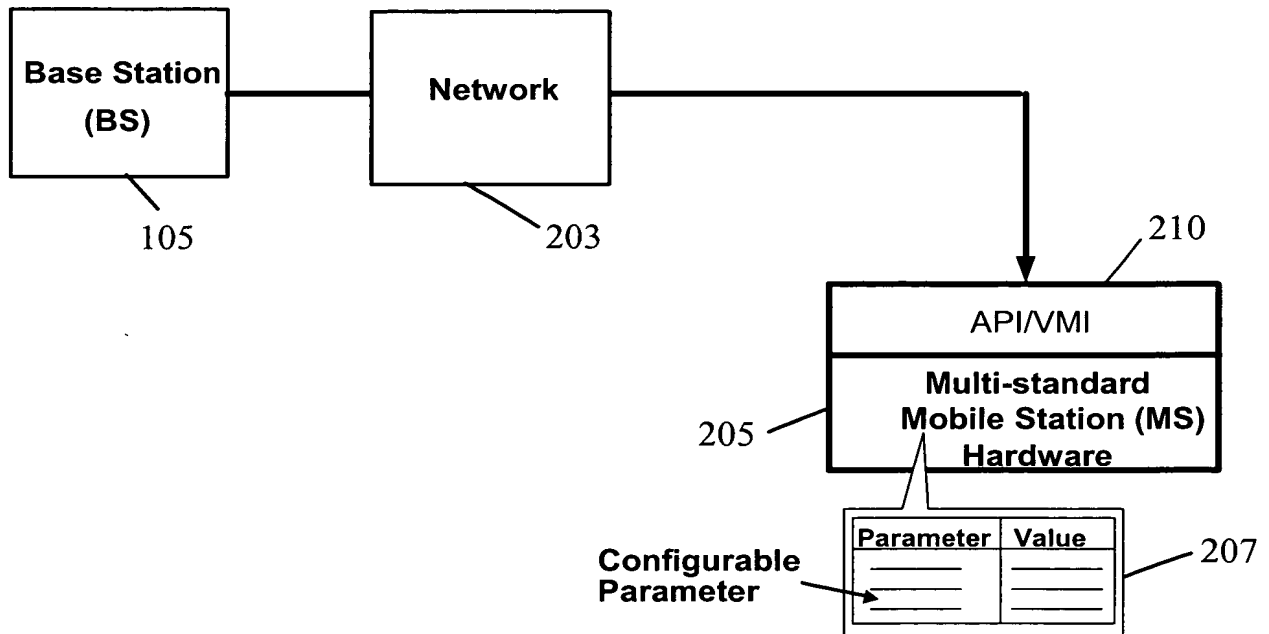


FIG. 2

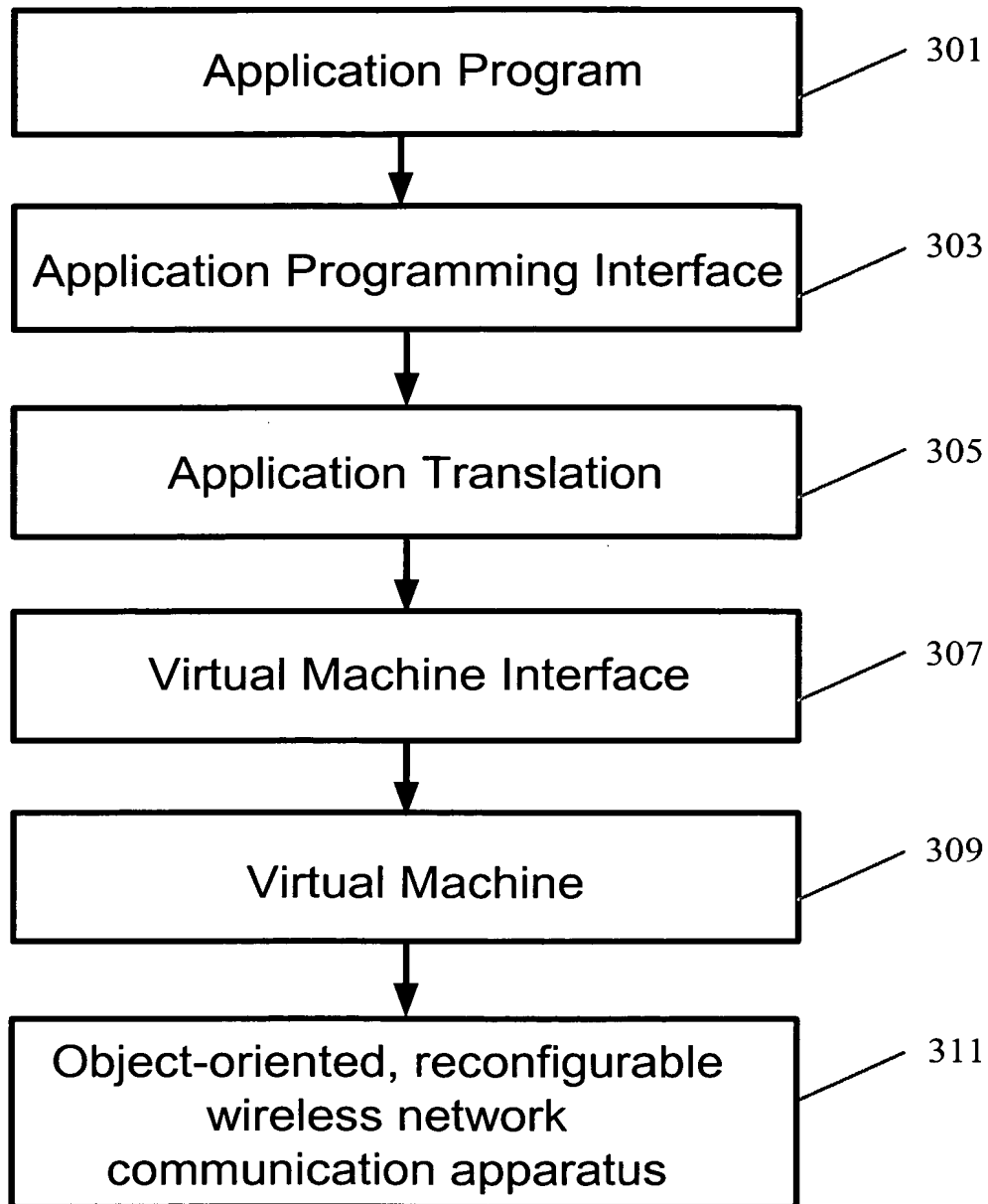


FIG. 3

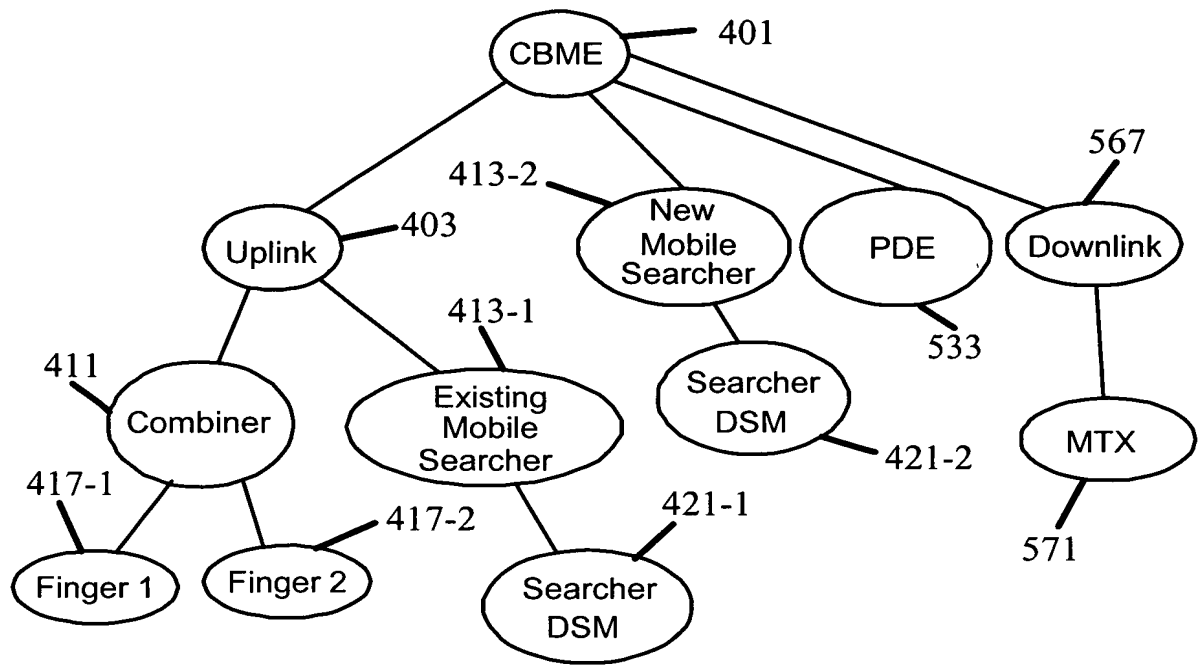


FIG. 4

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<b>CBME</b>	401
CBME_New	503
CBME_Set_User-Data	505
<b>CGU</b>	507
CGU_New	509
CGU_Set_User_Data	511
<b>Searcher</b>	413
Searcher_New	515
Set_Searcher_Static_Attributes	517
Set_Searcher_Dynamic_Attributes	519
Searcher_Start	521
Searcher_Stop	523
Assign_DSM_To_Searcher	525
<b>Searcher DSM</b>	421
Searcher_DSM_New	529
Set_DSM_State_Attributes	531
<b>Preamble Detection Engine (PDE)</b>	533
PDE_New	535
PDE_Add_Antenna	537
<b>Finger</b>	417
Finger_New	541
Set_Finger_Static_Attributes	543
Set_Finger_Dynamic_Attributes	545
Finger_Start	547
Finger_Stop	549
<b>Combiner</b>	411
Combiner_New	553
Add_Finger_To_Combiner	555
Remove_Finger_From_Combiner	557
Combiner_Start	559
Combiner_Stop	561
<b>Uplink</b>	403
Uplink_New	565
<b>Downlink</b>	567
Downlink_New	569
<b>Tx Multicode Channel (MTX)</b>	571
MTX_New	573

FIG. 5

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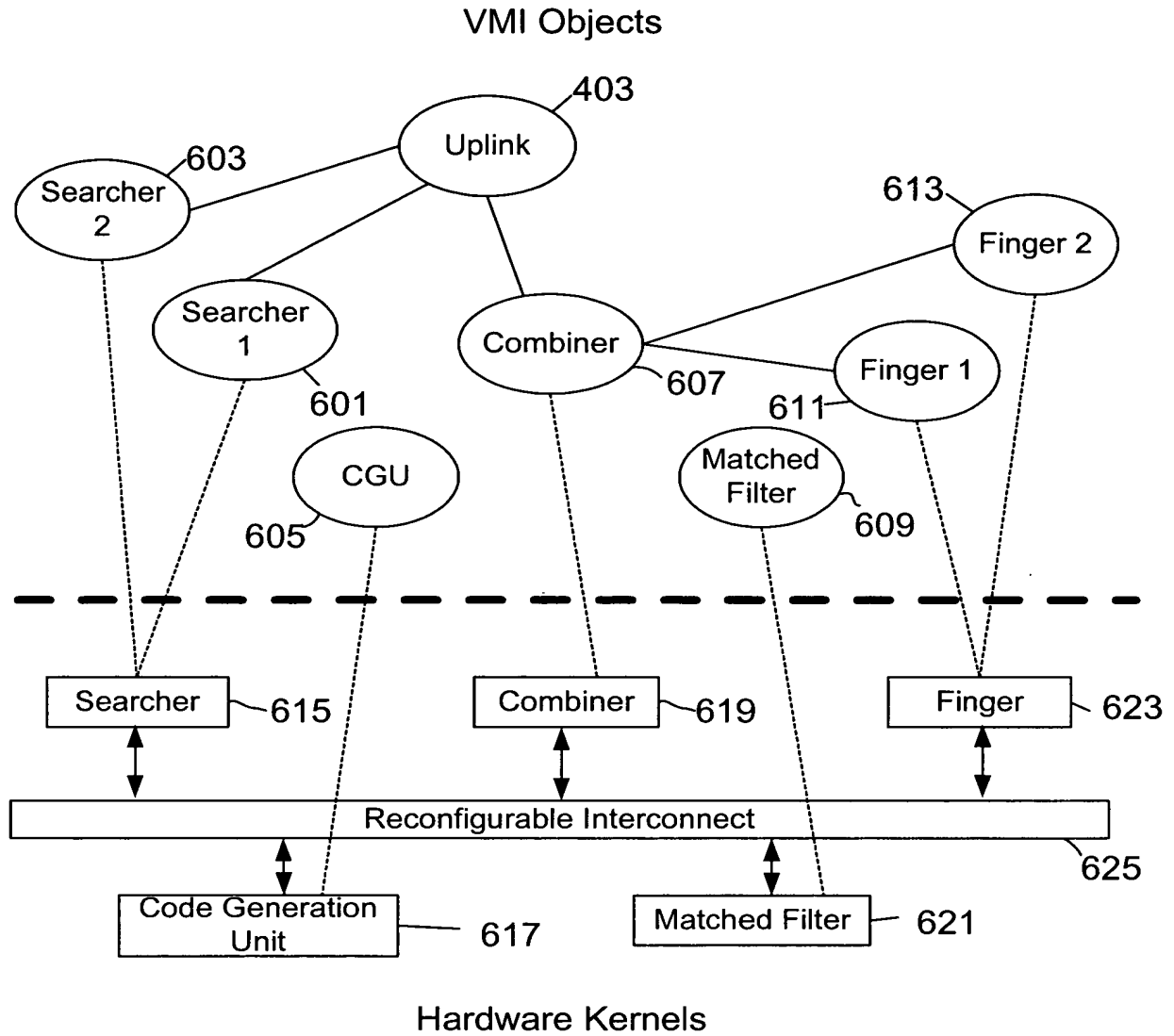


FIG. 6

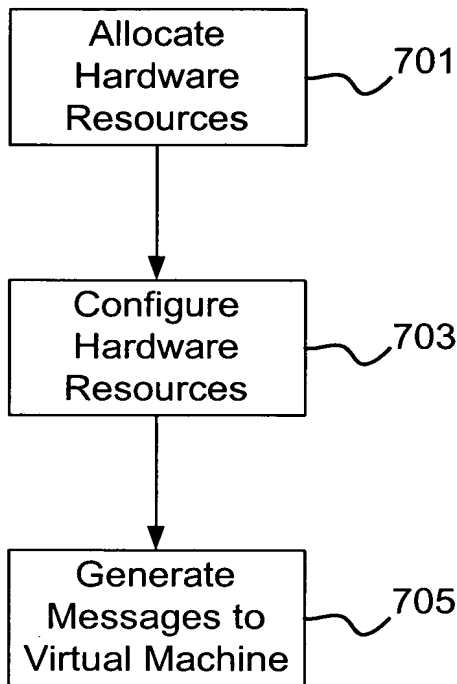


FIG. 7(A)

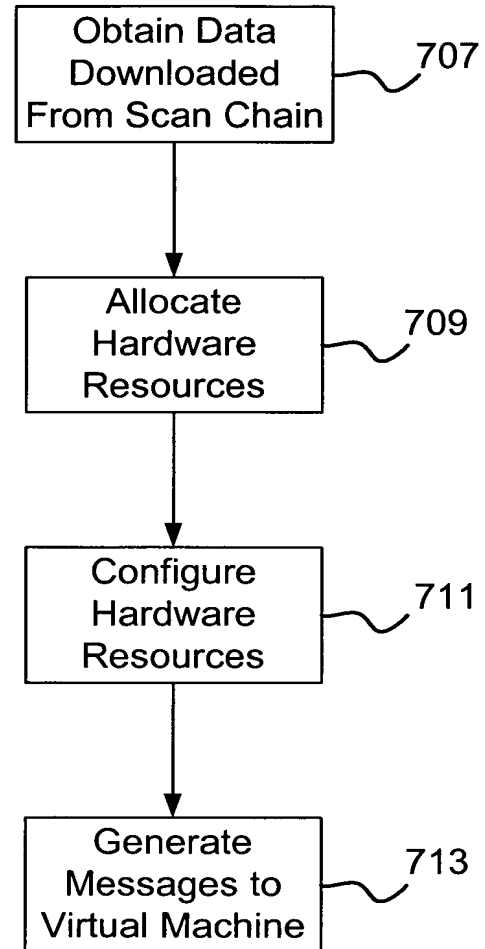


FIG. 7(B)

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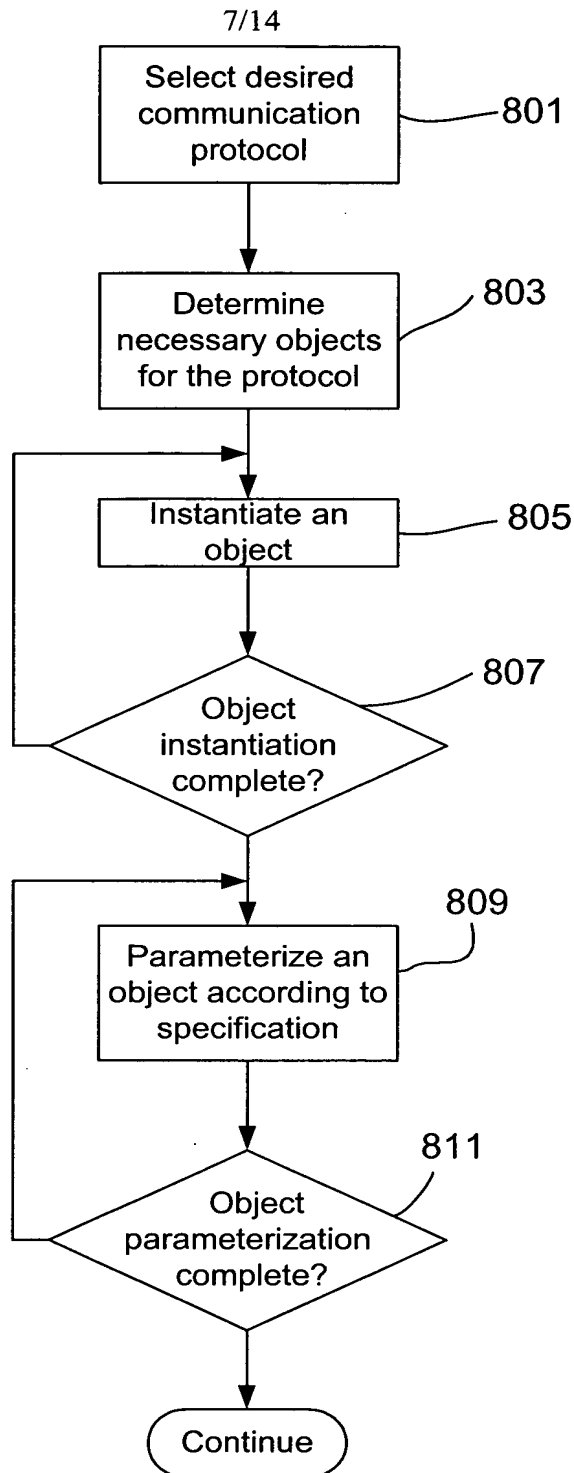


FIG. 8

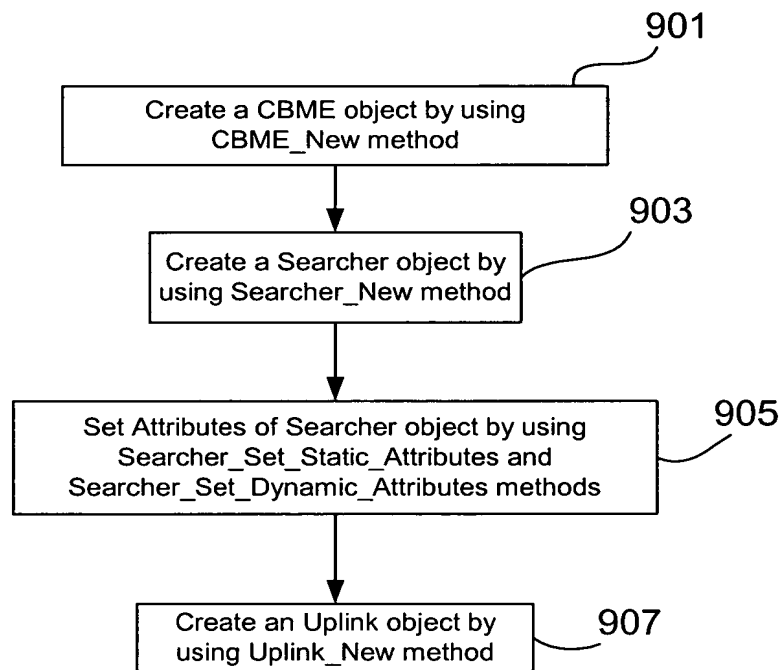


FIG. 9

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T05040" T8282860

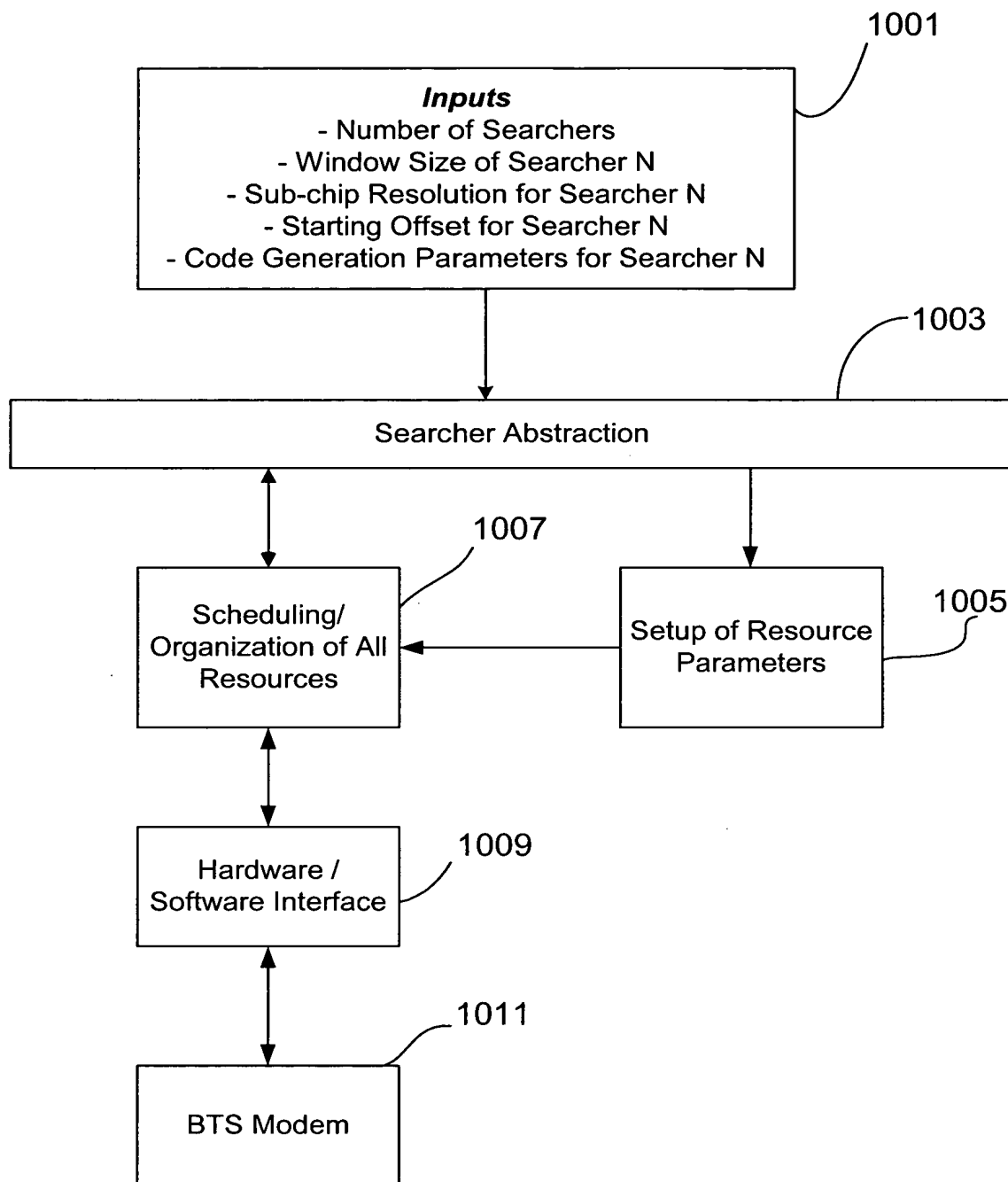


FIG. 10

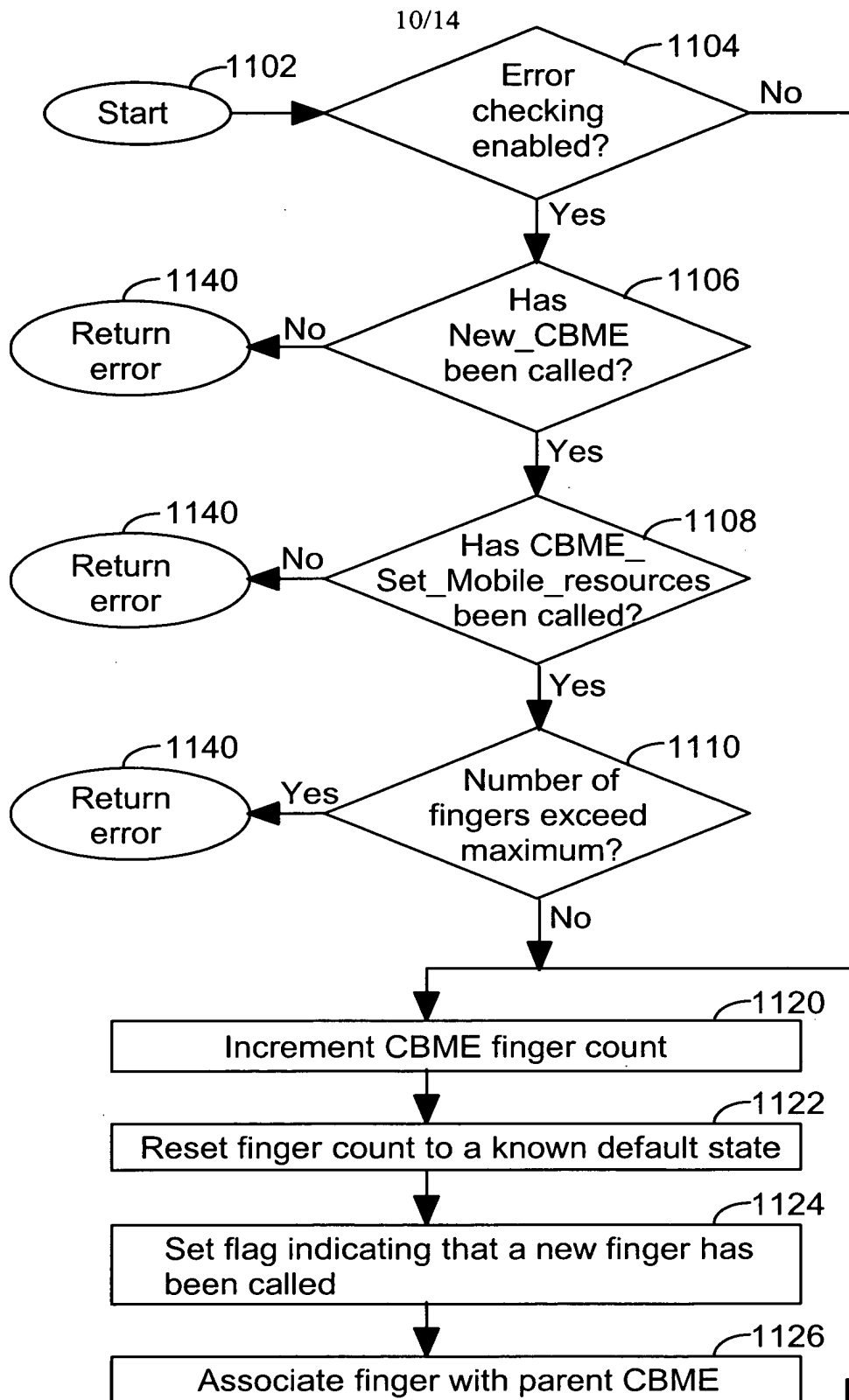
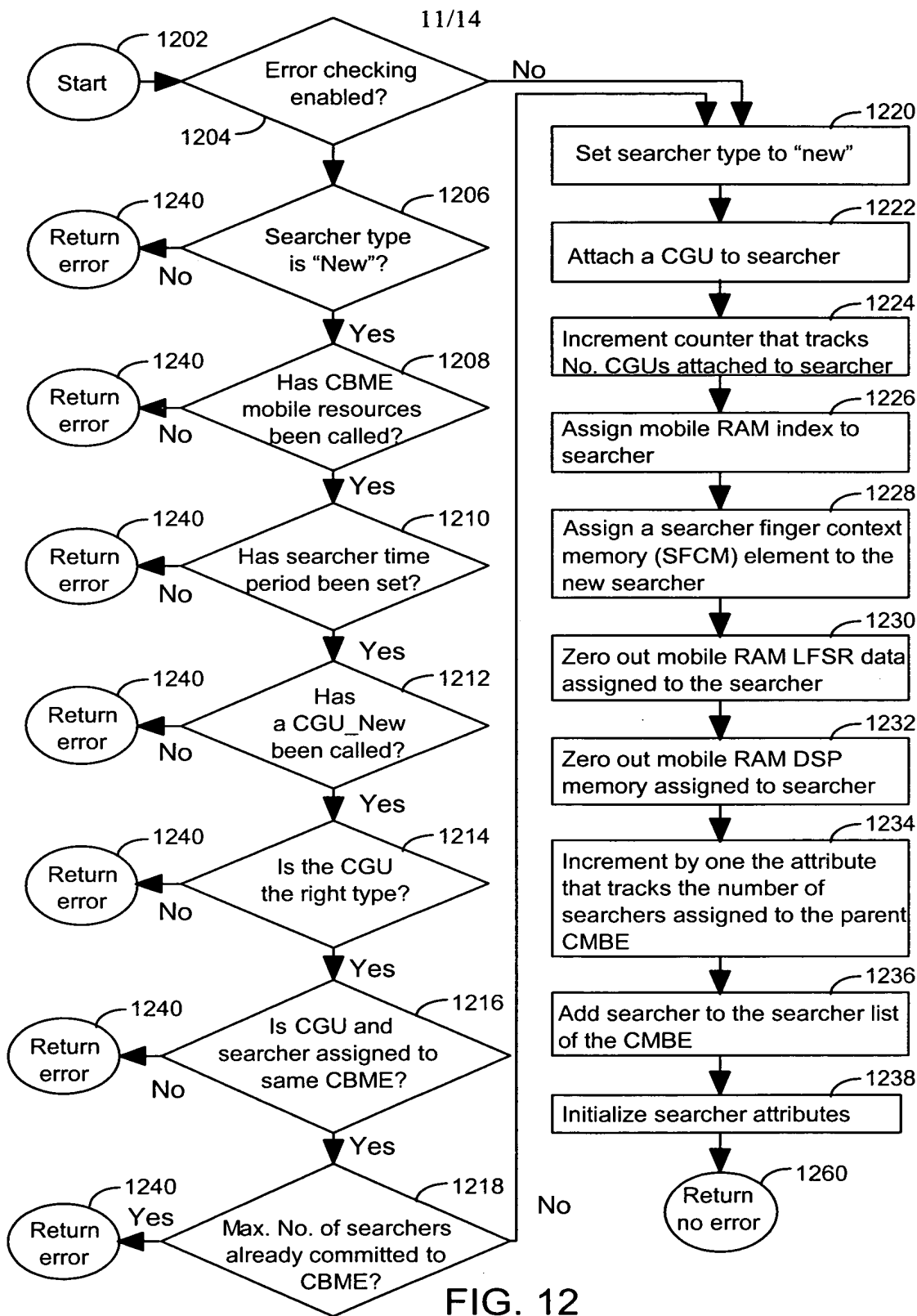


FIG. 11

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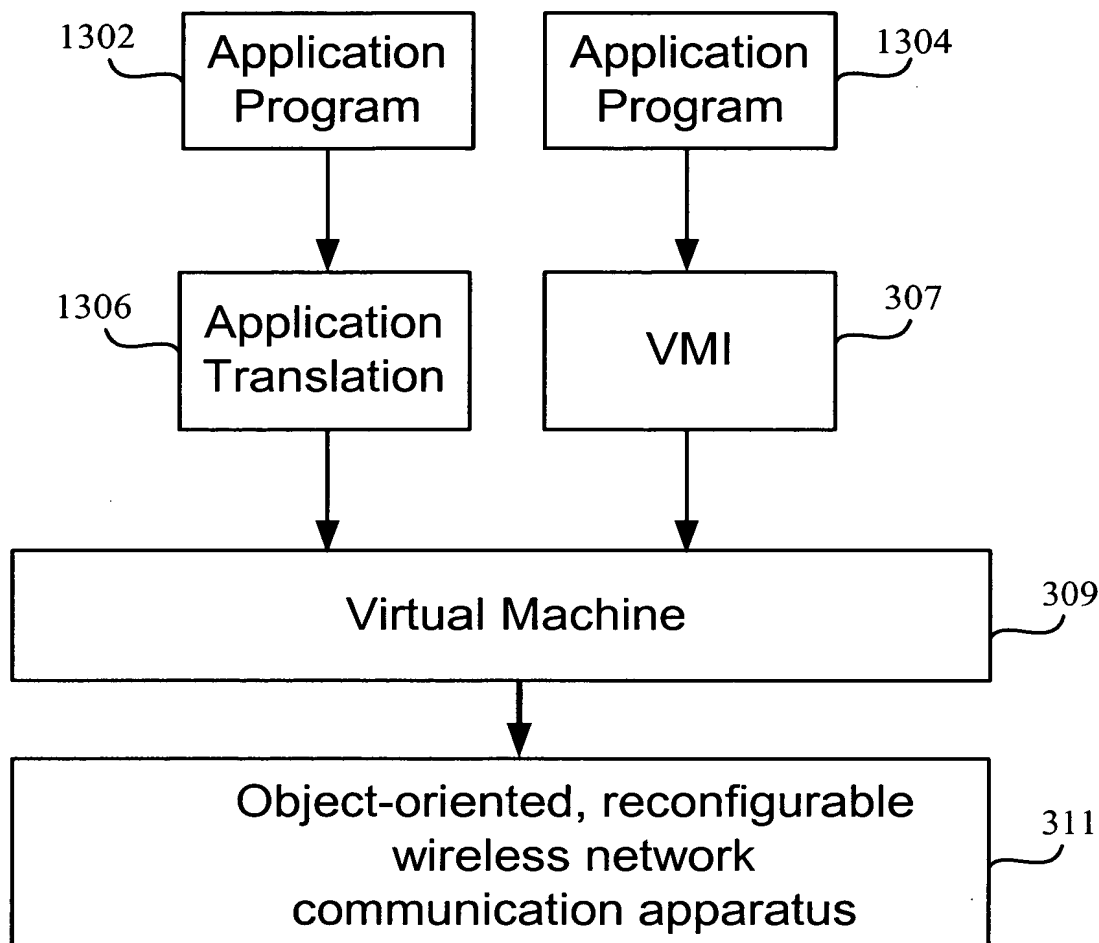


FIG. 13

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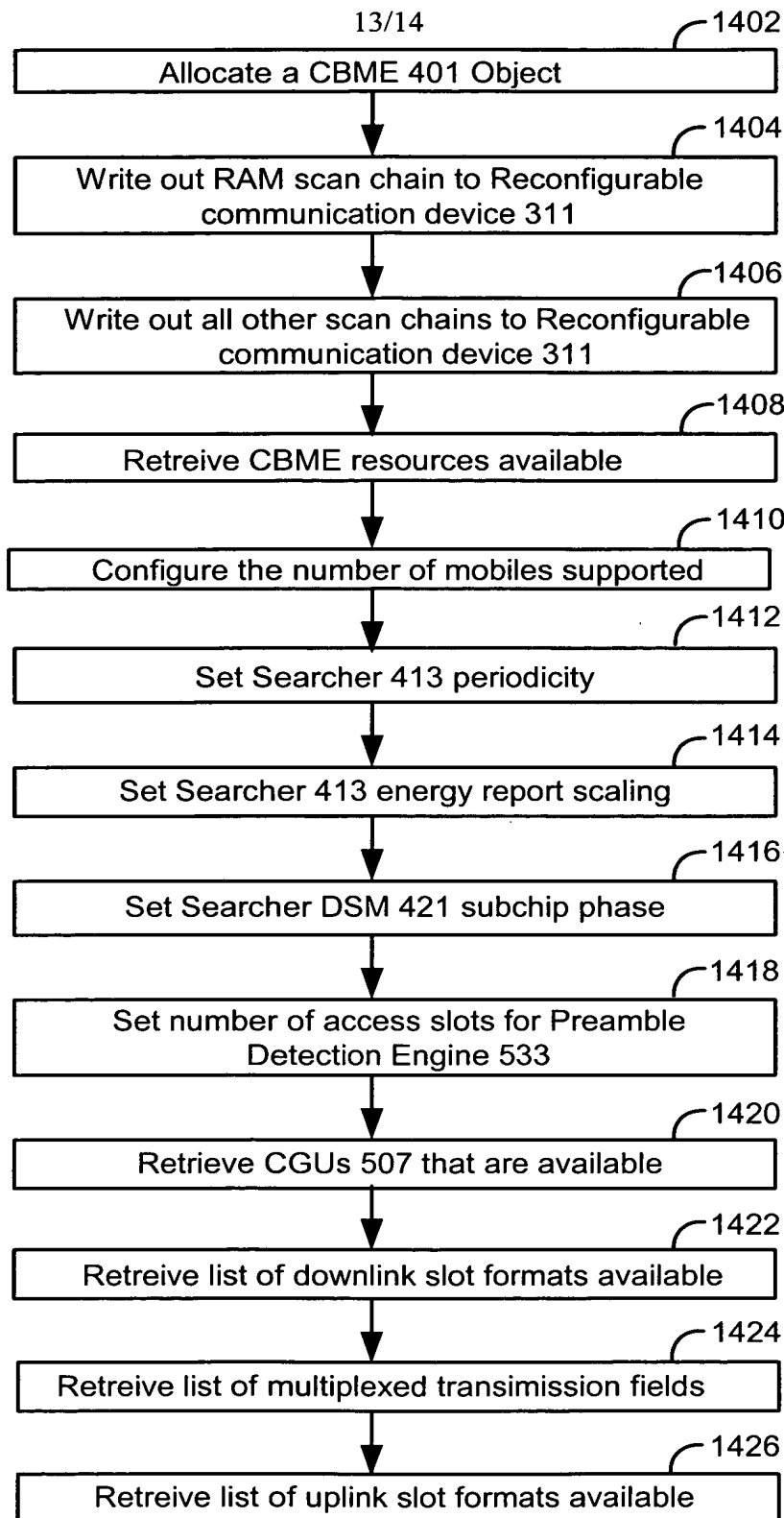


FIG. 14

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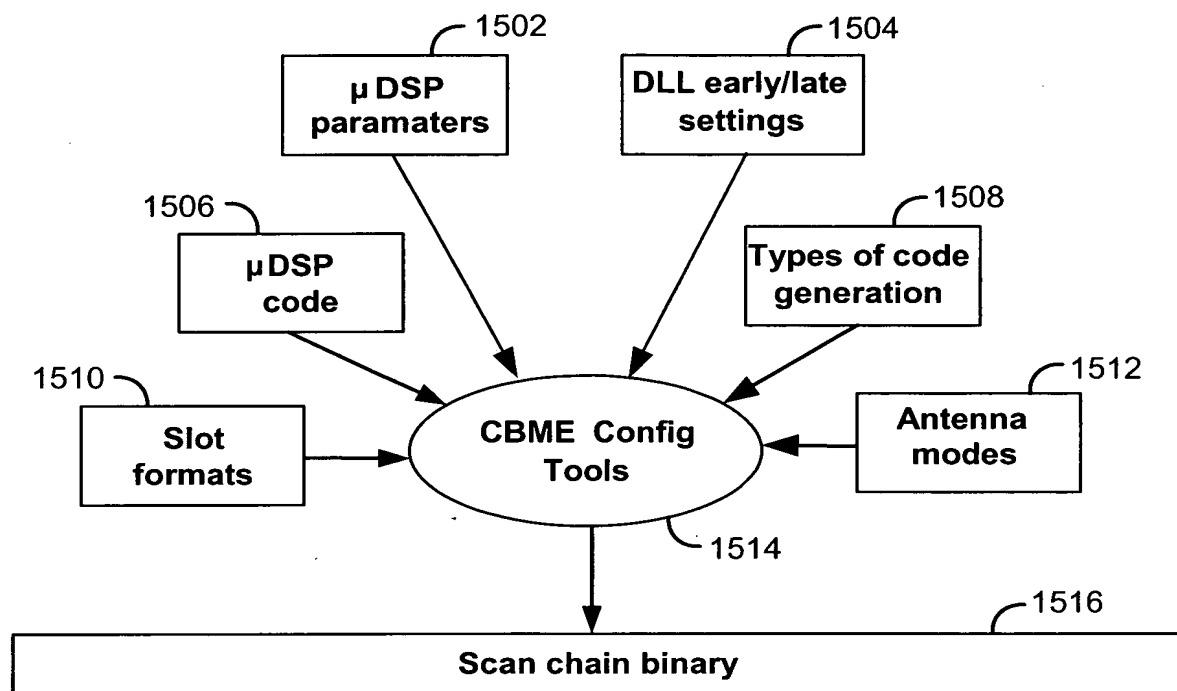


FIG. 15